

- Main Features
- D-TPM2.0 is a docking type TPM2.0 module
 - High-end security controller with advanced cryptographic algorithms implemented in hardware
 - TCG and Common Criteria certified with EAL4+
 - Flexible with LPC interface support and communicate with the serial interrupt(SERIRQ) protocol
 - Extended temperature range (-40 to +85℃) for a variety of applications
 - Industrial design, manufactured in Cadmus Taiwan

Product Overview

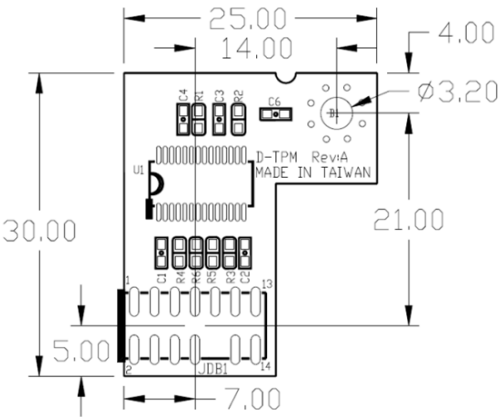
TPM (Trusted Platform Module) is a computer chip (microcontroller) that can securely store artifacts used to authenticate the platform (your PC or laptop). These artifacts can include passwords, certificates, or encryption keys.

A TPM can also be used to store platform measurements that help ensure that the platform remains trustworthy. Authentication (ensuring that the platform can prove that it is what it claims to be) and attestation (a process helping to prove that a platform is trustworthy and has not been breached) are necessary steps to ensure safer computing in all environments.

Specifications

Module Name	D-TPM2.0
Board Layout	30 x 15 mm thickness: 1.6mm OSP
Chipset	TPM2.0 : SLB9665TT2.0 FW5.62 TPM1.2 : SLB9660TT1.2 FW4.43
Input interface	LPC interface
Support	TPM1.2 Supports : RSA encryption RSA signature RSA-DAA SHA-1 HMAC One-time-pad with XOR AES (optional) TPM2.0 Supports : RSA encryption and signature ECC encryption and signature ECC-DAA ECDH SHA-1, SHA-256 HMAC AES and one-time-pad with XOR
Compatible Operating System	Windows 7 / Window 8 / Windows 8.1 64 bit / Windows 10 64 bit / Linux Kernel Version 3.10 and higher
Relative Humidity	Operating 10%~90%, non-condensing Non-operating 5%~95%, non-condensing

Dimension



Ordering Information

D-TPM2.0

- RoHS Compliance
- TPM SLB 9665 TT2.0

D-TPM1.2

- RoHS Compliance
- TPM SLB 9660 TT1.2

Pin definition

FOR 80 port DEBUG CARD

CLK	■	■	■	■	LAD1
RST#	■	■	■	■	LAD0
FRAME#	■	■	■	■	VCC3
LAD3	■	■	■	■	GND
LAD2	■	■	■	■	GND
SERIRQ	■	■	■	■	GND
5VSB	■	■	■	■	5V